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### 1 [Demand-driven structural testing with dynamic instrumentation](#)

Jonathan Misurda, James A. Clause, Juliya L. Reed, Bruce R. Childers, Mary Lou Soffa  
May I CSE '05: Proceedings of the 27th international conference on Software engineering  
2005  
**Publisher:** ACM

Full text available: [pdf\(180.97 KB\)](#) [Publisher Site](#)

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Producing reliable and robust software has become one of the most important software development concerns in recent years. Testing is a process by which software quality can be assured through the collection of information. While testing can improve ...



**Keywords:** Java programming language, code coverage, demand-driven instrumentation, structural testing, testing

## 2 Dynamic Conditional Branch Balancing during the High-Level Synthesis of Control-Intensive Designs

Sumit Gupta, Nikil Dutt, Rajesh Gupta, Alex Nicolau

March 2003 DATE '03: Proceedings of the conference on Design, Automation and Test in Europe - Volume 1, Volume 1


**Publisher:** IEEE Computer Society

Full text available:  [pdf\(175.61 KB\)](#)  [Publisher Site](#)



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We present two novel strategies to increase the scope for application of speculative code motions: (1) Adding scheduling steps dynamically during scheduling to conditional branches with fewer scheduling steps. This increases the opportunities to apply ...

## 3 Beyond templates: a study of clones in the STL and some general implications

 Hamid Abdul Basit, Damith C. Rajapakse, Stan Jarzabek  
May 2005 I CSE '05: Proceedings of the 27th international conference on Software engineering

**Publisher:** ACM


Full text available:  [pdf\(290.37 KB\)](#)  [Publisher Site](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)


Templates (or generics) help us write compact, generic code, which aids both reuse and maintenance. The STL is a powerful example of how templates help achieve these goals. Still, our study of the STL revealed substantial, and in our opinion, counter-productive ...

Keywords: clones, meta-programming, software maintenance

## 4 Fast data-locality profiling of native execution

 Erik Berg, Erik Hagersten  
June 2005 ACM SIGMETRI CS Performance Evaluation Review, Volume 33 Issue 1

**Publisher:** ACM

Full text available:  [pdf\(349.73 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Performance tools based on hardware counters can efficiently profile the cache behavior of an application and help software developers improve its cache utilization. Simulator-based tools can potentially provide more insights and flexibility and model ...

Keywords: cache behavior, profiling tool

## 5 [Fast data-locality profiling of native execution](#)



Erik Berg, Erik Hagersten

June 2005 SIGMETRICS '05: Proceedings of the 2005 ACM SIGMETRICS international conference on Measurement and modeling of computer systems

**Publisher:** ACM

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Performance tools based on hardware counters can efficiently profile the cache behavior of an application and help software developers improve its cache utilization. Simulator-based tools can potentially provide more insights and flexibility and model ...

Keyw ords: cache behavior, profiling tool

## 6 [Improving software security with a C pointer analysis](#)



Dzintars Avots, Michael Dalton, V. Benjamin Livshits, Monica S. Lam

May 2005 ICSE '05: Proceedings of the 27th international conference on Software engineering

**Publisher:** ACM

Full text available: [pdf\(181.96 KB\)](#) [Publisher Site](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

This paper presents a context-sensitive, inclusion-based, field-sensitive points-to analysis for C and uses the analysis to detect and prevent security vulnerabilities in programs. In addition to a conservative analysis, we propose an optimistic analysis ...

Keyw ords: buffer overflows, context-sensitive, dynamic analysis, error detection, format string violations, pointer analysis, program analysis, programming languages, security aws, software errors, software security, type safety, vulnerabilities

## 7 [Optimizing queries using materialized views: a practical, scalable solution](#)



Jonathan Goldstein, Per-Åke Larson

June 2001 ACM SIGMOD Record, Volume 30 Issue 2

**Publisher:** ACM

Full text available: [pdf\(202.08 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)

Materialized views can provide massive improvements in query processing time, especially for aggregation queries over large tables. To realize this potential, the query optimizer must know how and when to exploit materialized views. This paper presents ...

Keyw ords: materialized views, query optimization, view matching

## 8 [Optimizing queries using materialized views: a practical, scalable solution](#)



Jonathan Goldstein, Per-Åke Larson

May SIGMOD '01: Proceedings of the 2001 ACM SIGMOD international conference  
2001 on Management of data

**Publisher:** ACM

Full text available: [pdf\(202.08 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),  
[index terms](#), [review](#)

Materialized views can provide massive improvements in query processing time, especially for aggregation queries over large tables. To realize this potential, the query optimizer must know how and when to exploit materialized views. This paper presents ...

**Keywords:** materialized views, query optimization, view matching

## 9 [Cache Simulation Based on Runtime Instrumentation for OpenMP Applications](#)

Jie Tao, Josef Weidendorfer

April ANSS '04: Proceedings of the 37th annual symposium on Simulation  
2004

**Publisher:** IEEE Computer Society

Full text available: [pdf\(150.92 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),  
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To enable optimizations in memory access behavior of high performance applications, cache monitoring is a crucial process. Simulation of cache hardware is needed in order to allow research for non-existing cache architectures, and on the other hand, to get ...

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